

## PATENT ABSTRACTS OF JAPAN

(11)Publication number : 11-265283

(43)Date of publication of application : 28.09.1999

(51)Int. Cl.

G06F 9/06

G06F 9/22

G06F 12/06

(21)Application number : 10-068102

(71)Applicant : HITACHI LTD

(22)Date of filing : 18.03.1998

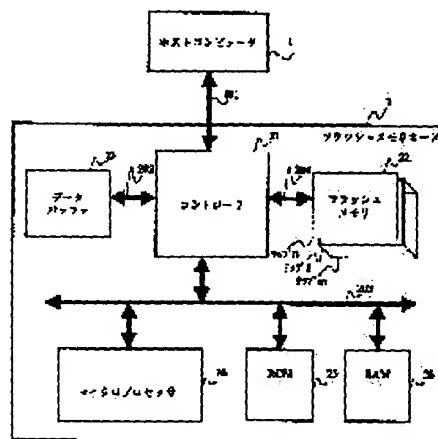
(72)Inventor : TAMURA TAKAYUKI  
KATAYAMA KUNIHIRO  
NAKAMURA KAZUO

(54) CORRECTION METHOD FOR FIRMWARE IN STORAGE DEVICE, AND STORAGE DEVICE

(57)Abstract:

PROBLEM TO BE SOLVED: To make easily correctable a firmware even in the case where a failure is generated in the firmware by correcting a module and a jump table where the failure is generated and storing them in a flash memory.

SOLUTION: In the case where a failure is generated in a firmware stored in a ROM 25, a corrected module and a corrected jump table are stored in the flash memory 22. After copying the jump table stored in the ROM 25 to a RAM 26, the corrected jump table and the plural modules stored in the flash memory 22 are loaded to the RAM 26. At the time, the jump table copied from the ROM 25 is rewritten to the corrected jump table stored in the flash memory 22. Thus, the module where the failure is generated and stored in the ROM 25 is not referred to and the corrected module loaded to the RAM 26 is referred to.



## LEGAL STATUS

[Date of request for examination]

07.09.2001

[Date of sending the examiner's  
decision of rejection][Kind of final disposal of application  
other than the examiner's decision of  
rejection or application converted  
registration][Date of final disposal for  
application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's  
decision of rejection]

[Date of requesting appeal against  
examiner's decision of rejection]

[Date of extinction of right]

Copyright (C) ; 1998, 2003 Japan Patent Office